

FIG. 1

Lead-in	...
	DMA2
	Write Condition Test Area
	Primary TDMA
	DMA1
	Drive Information Area
	...
Data Area	Spare Area1
	Secondary TDMA
	User Data Area
	Spare Area2
Lead-out	...
	DMA3
	...
	DMA4
	...

FIG. 2

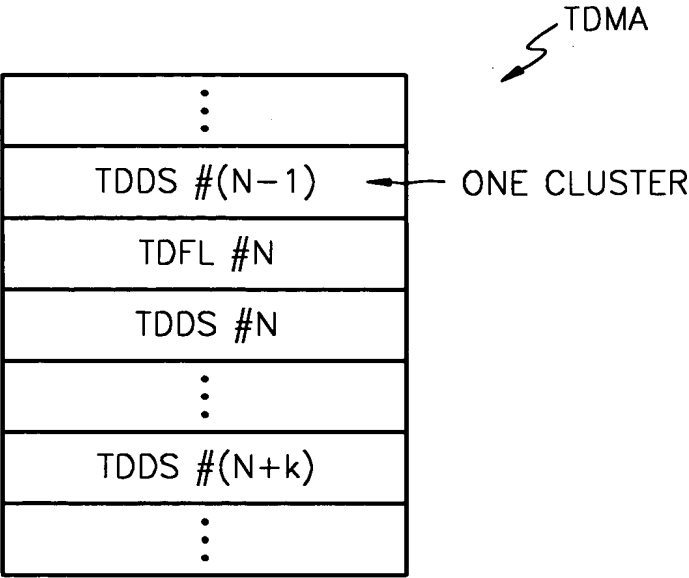


FIG. 3

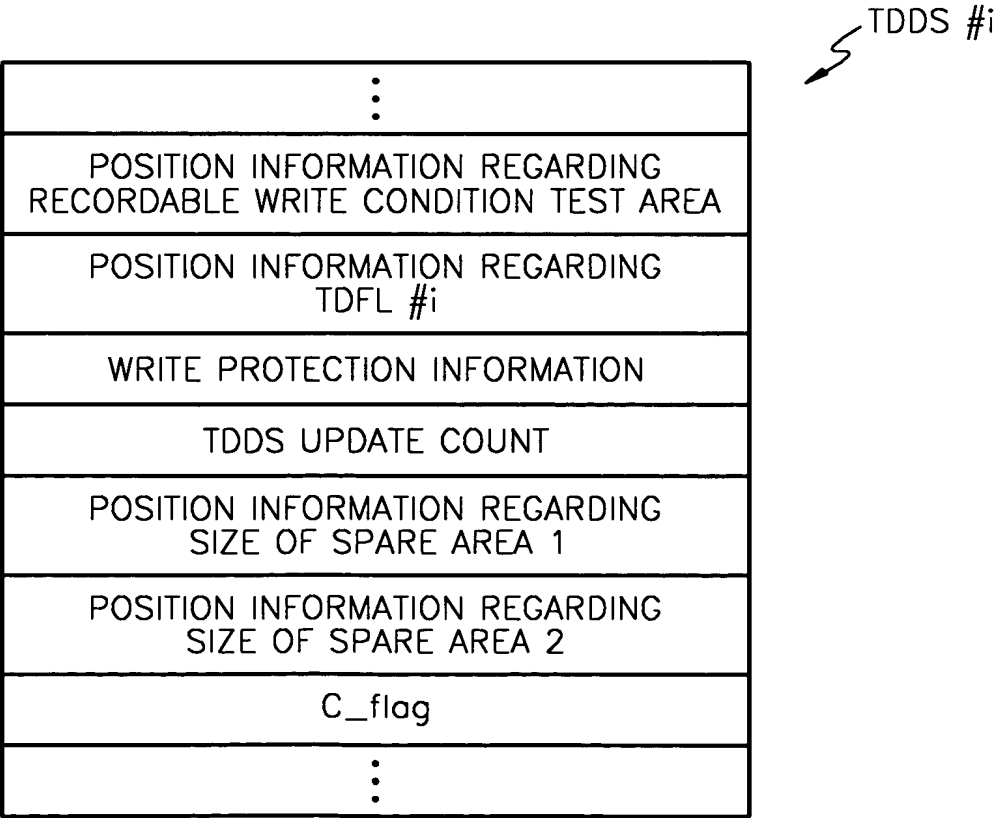


FIG. 4

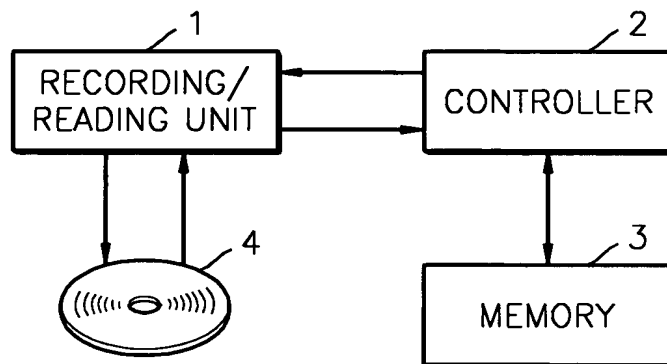


FIG. 5

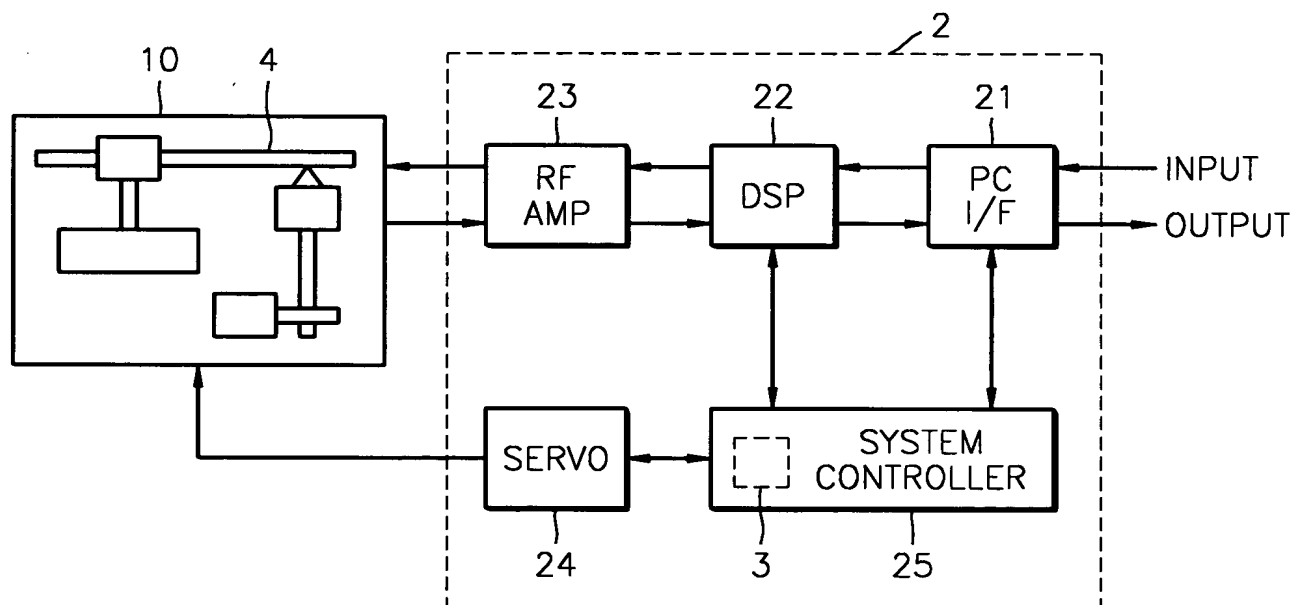


FIG. 6

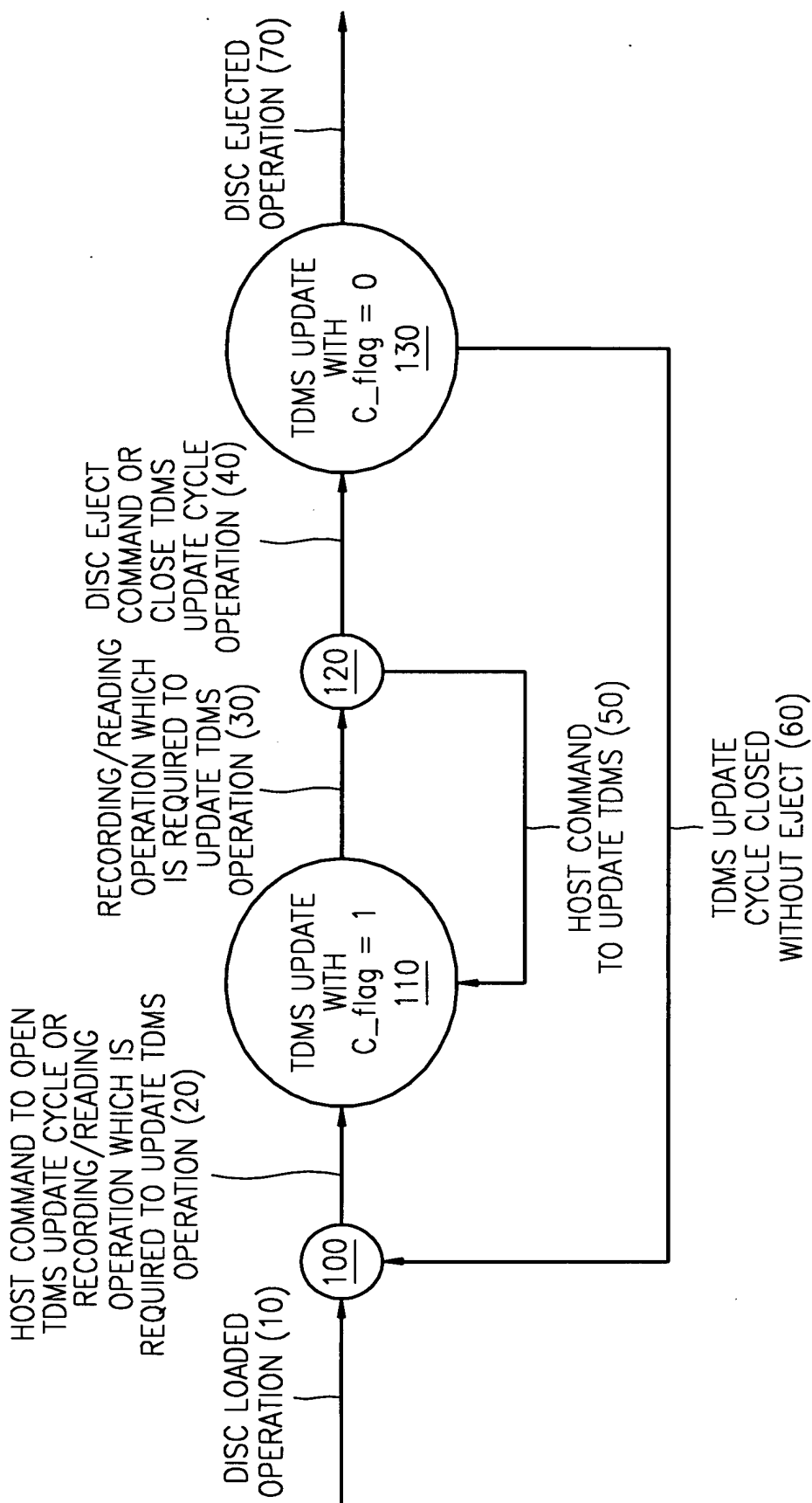


FIG. 7

